



**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

**In re Patent Application of:**

**Paul A. Swetland**

Serial No.: 10/715,750

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For: CIRCUIT BOARD

**CERTIFICATE OF MAILING**

I hereby certify that this paper is being deposited with the United States Postal Service as First-Class Mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, Virginia 22313-1450, on this date.

**Date: December 21, 2004**

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## **INFORMATION DISCLOSURE STATEMENT**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, Virginia 22313-1450

**Dear Sir:**

Applicant, in compliance with his duty of disclosure under Rule 56 is enclosing herewith a PTO Form 1449 listing U.S. Patent No. 6,650,548 B1 and a copy of U.S. Patent No. 6,650,548 B1.

U.S. Patent No. 6,650,548 B1 is the patent which issued on the parent application to the subject application. This patent was allowed in a first Action over all of the references listed in the patent which are also cited in the subject application.

Claims in the subject application are slightly different from the claims allowed in the parent application in that the claims in the subject application merely state that the connection locations are spaced from each other by a predetermined distance without defining that distance as a space. Further, the amended claims call for sets of connection locations in each row to be offset or staggered from each adjacent row such

that an array of spaces is formed with each space in an interior row forming a center of a diamond shaped four connection point connector that has a connection location to a conductive strip at each of it's four points or corners that originates from a different conductive strip.

This is the essence of Applicant's invention and distinguishes Applicant's circuit board from all the prior art cited.

Applicant submits that the subject application is in condition for allowance and an early and favorable action to that end is requested.

Respectfully submitted,

WELSH & KATZ, LTD.

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